

Figure 1

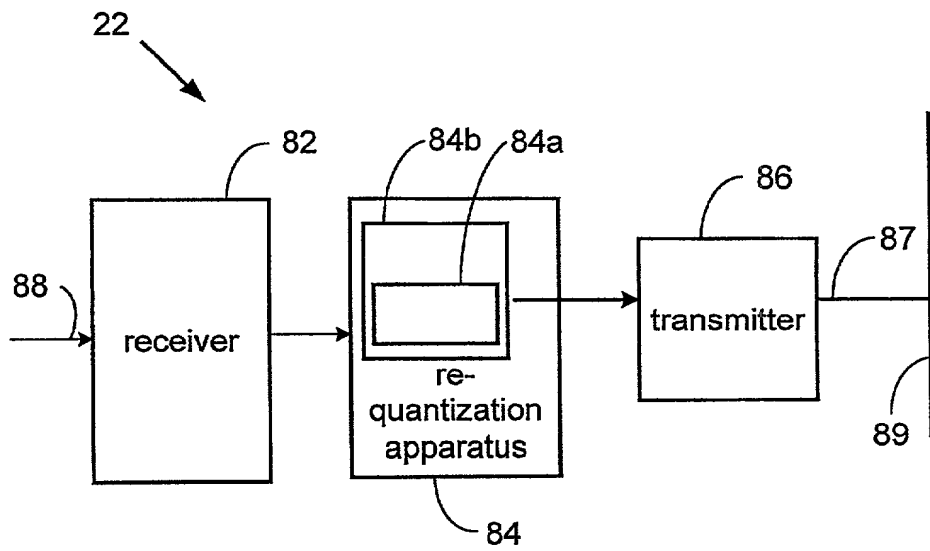


Figure 2A

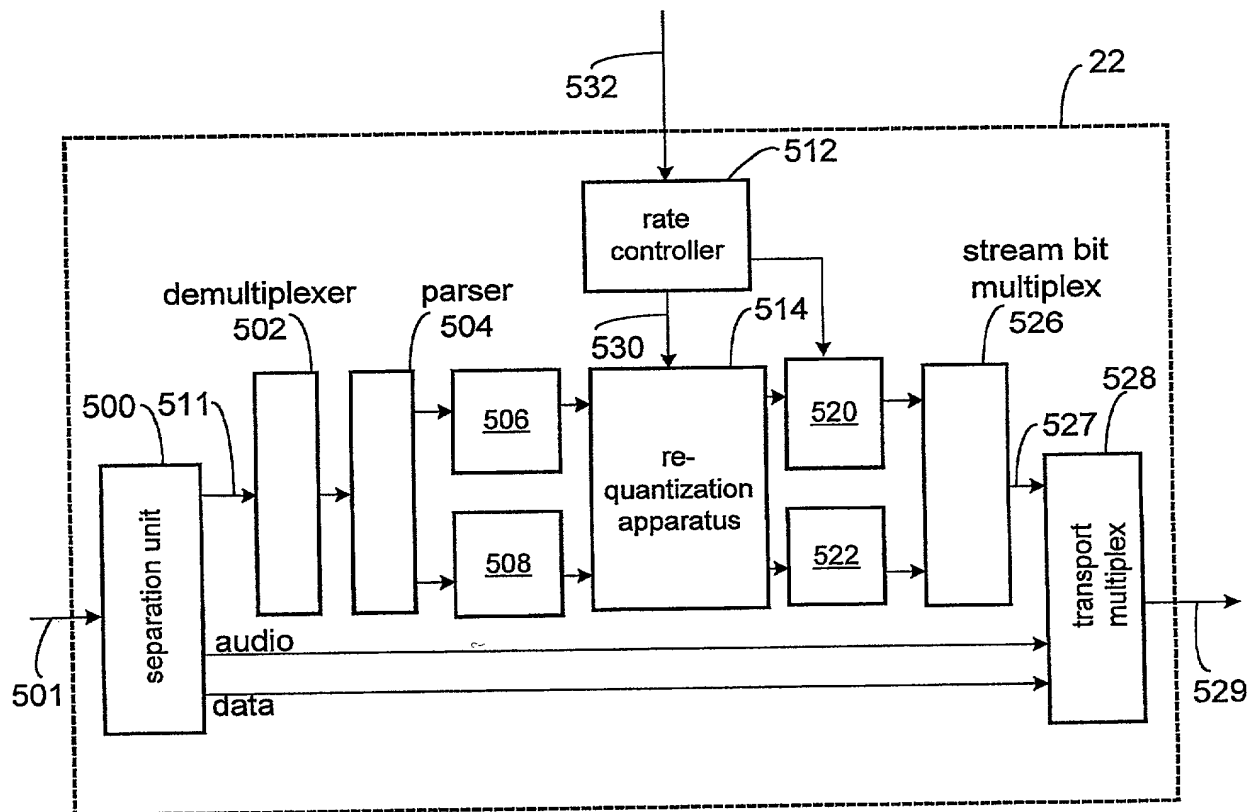


Figure 2B

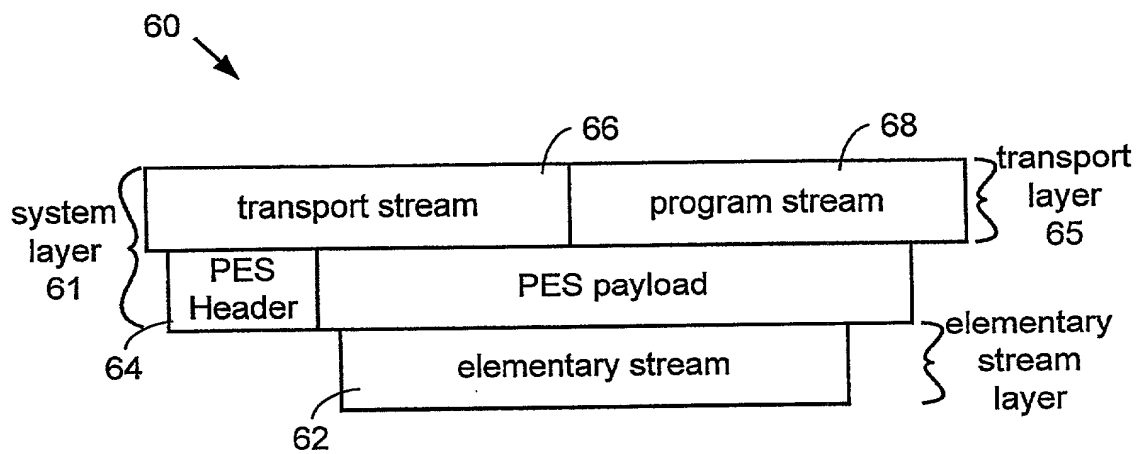


Figure 3

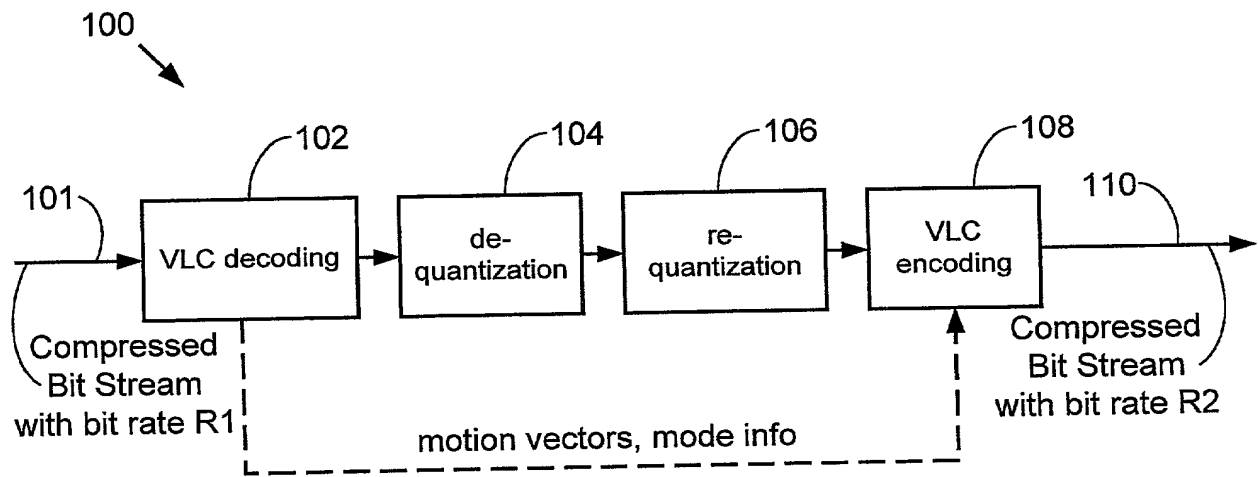


Figure 4A
(PRIOR ART)

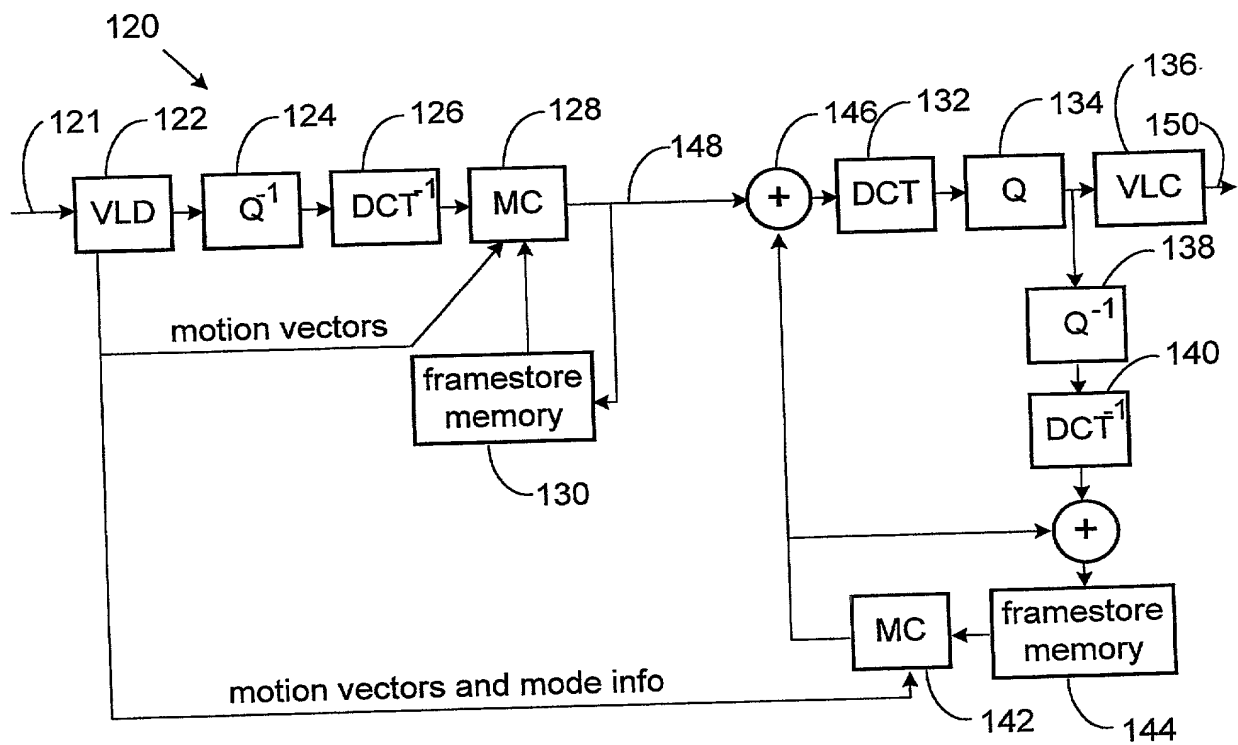


Figure 4B
(PRIOR ART)

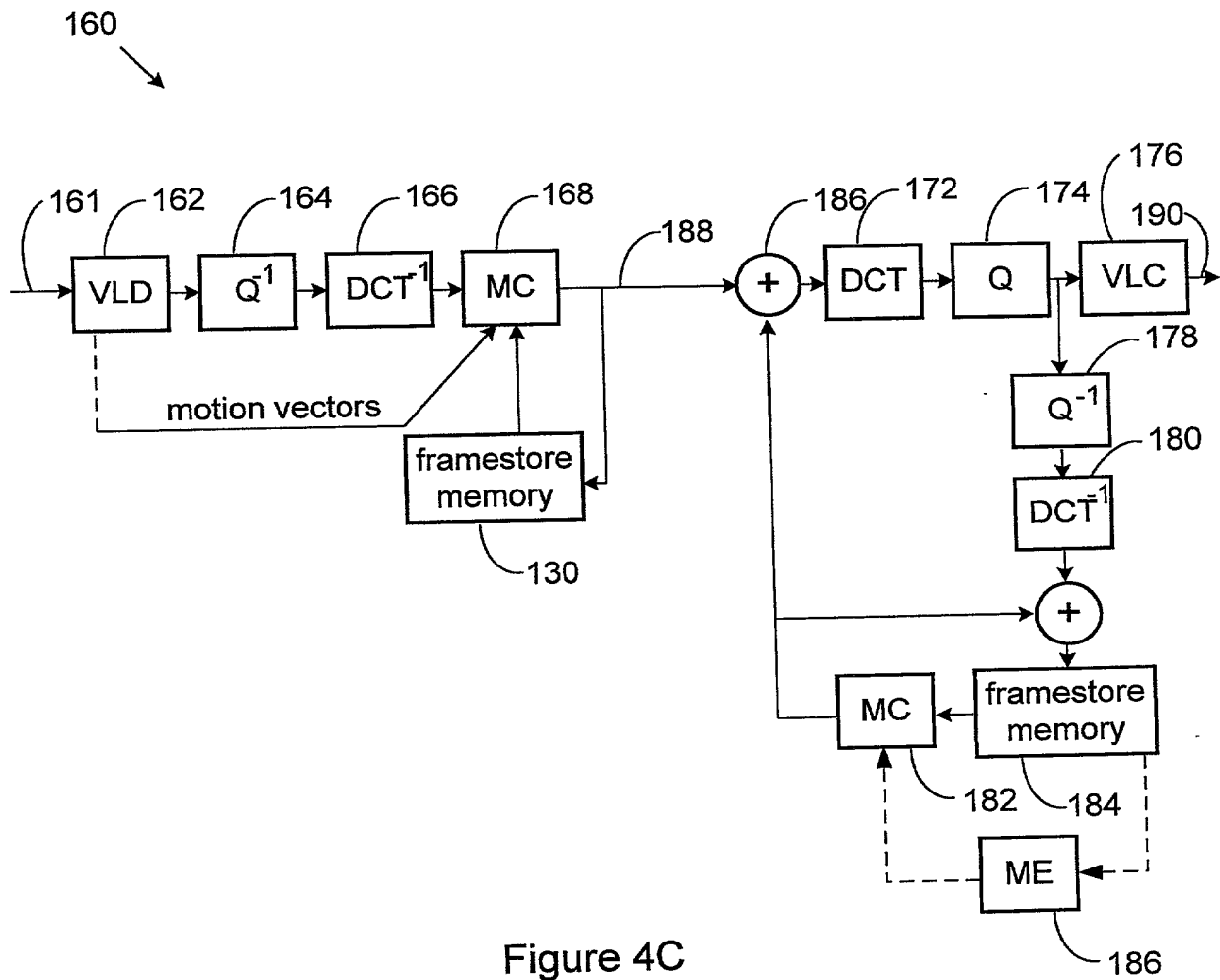


Figure 4C
(PRIOR ART)

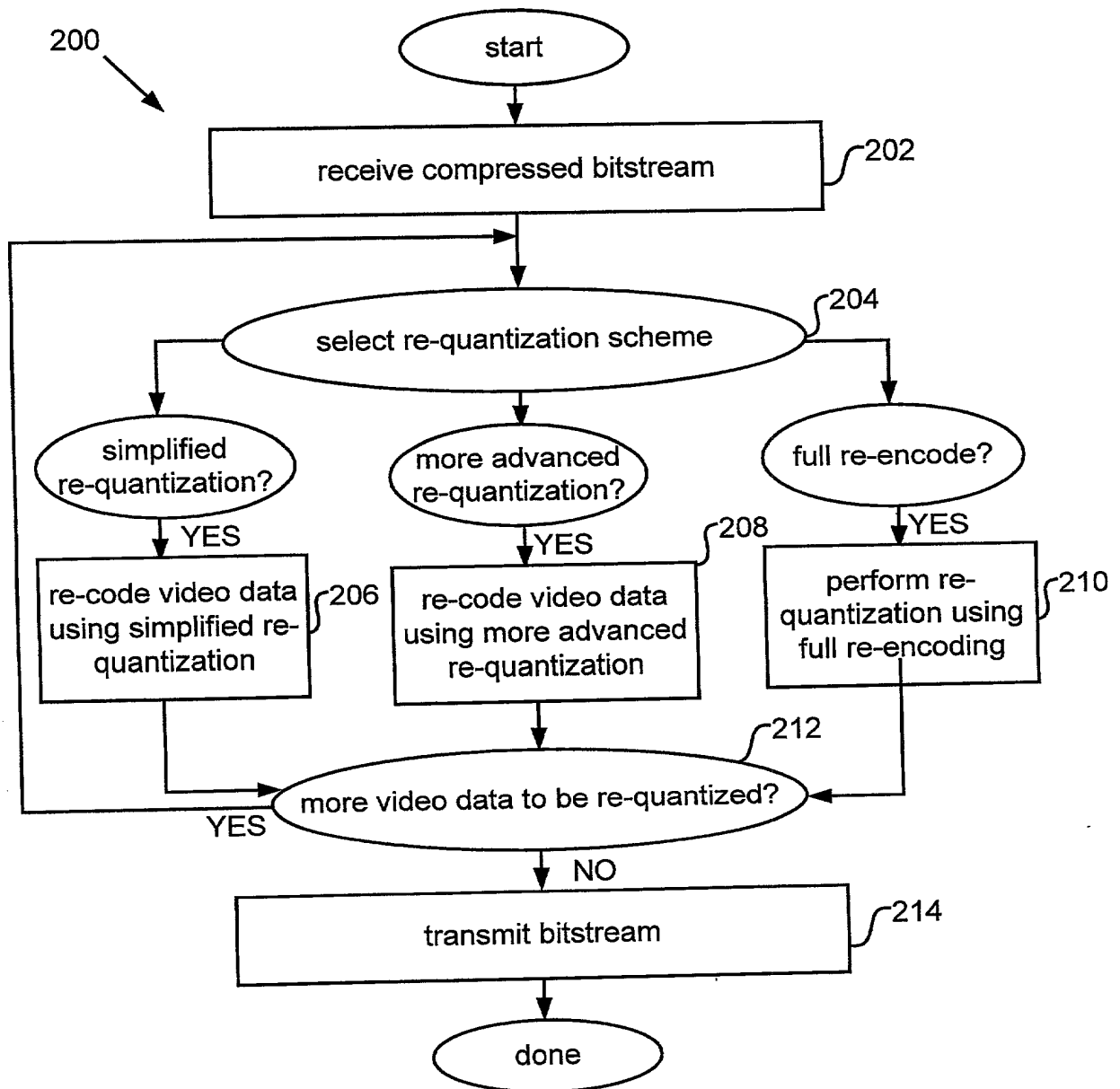


Figure 4D

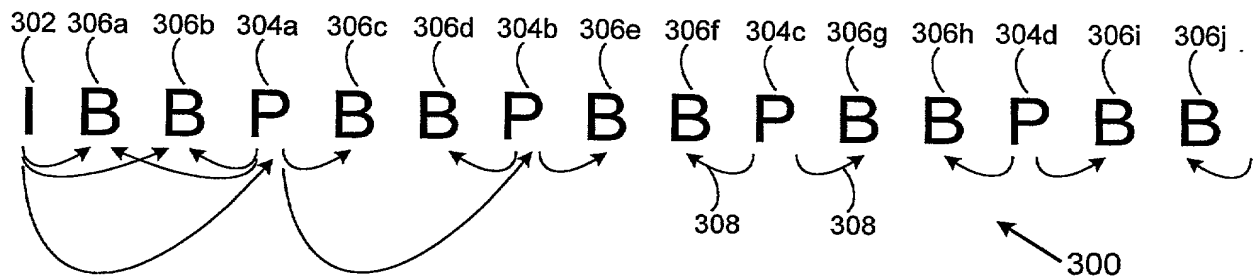


Figure 5A

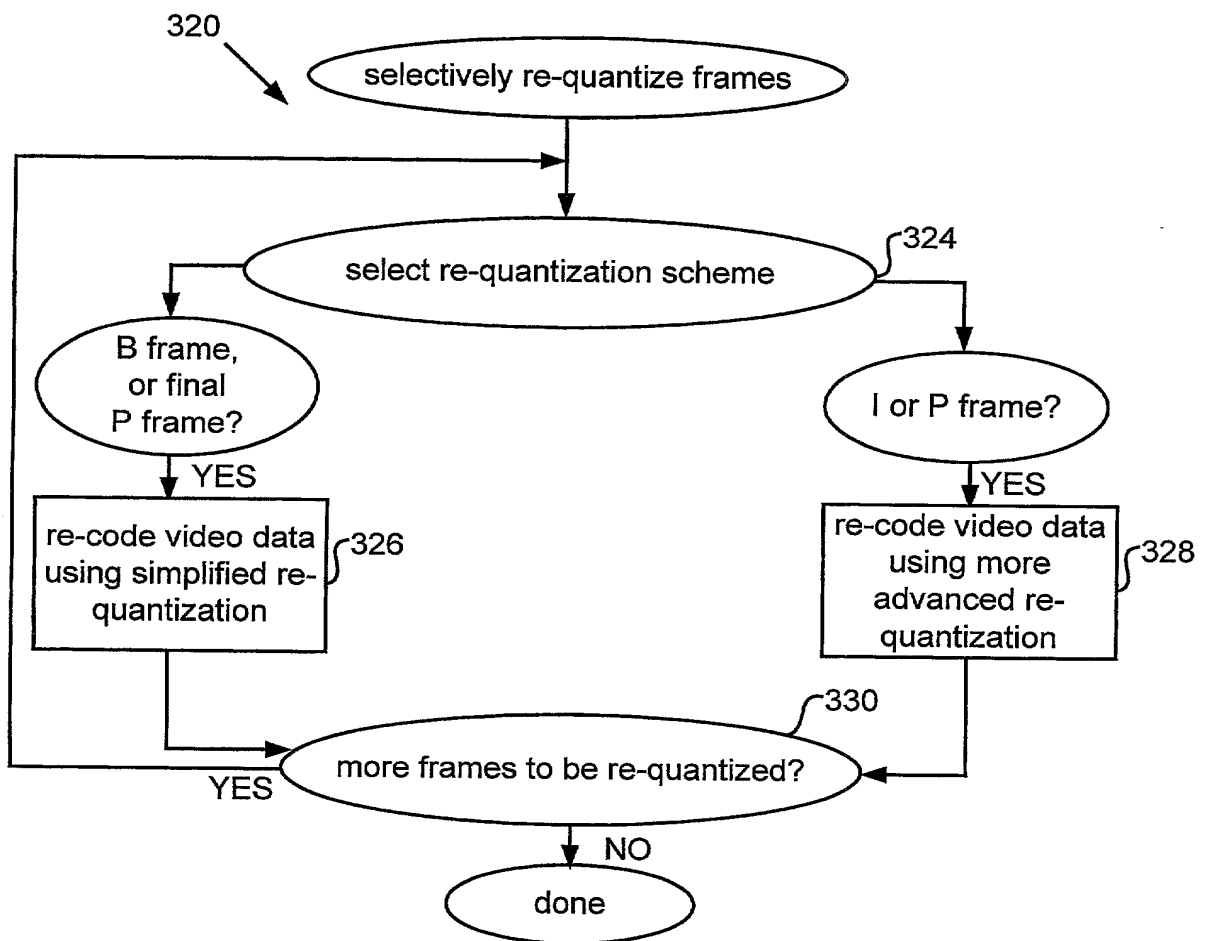


Figure 5B

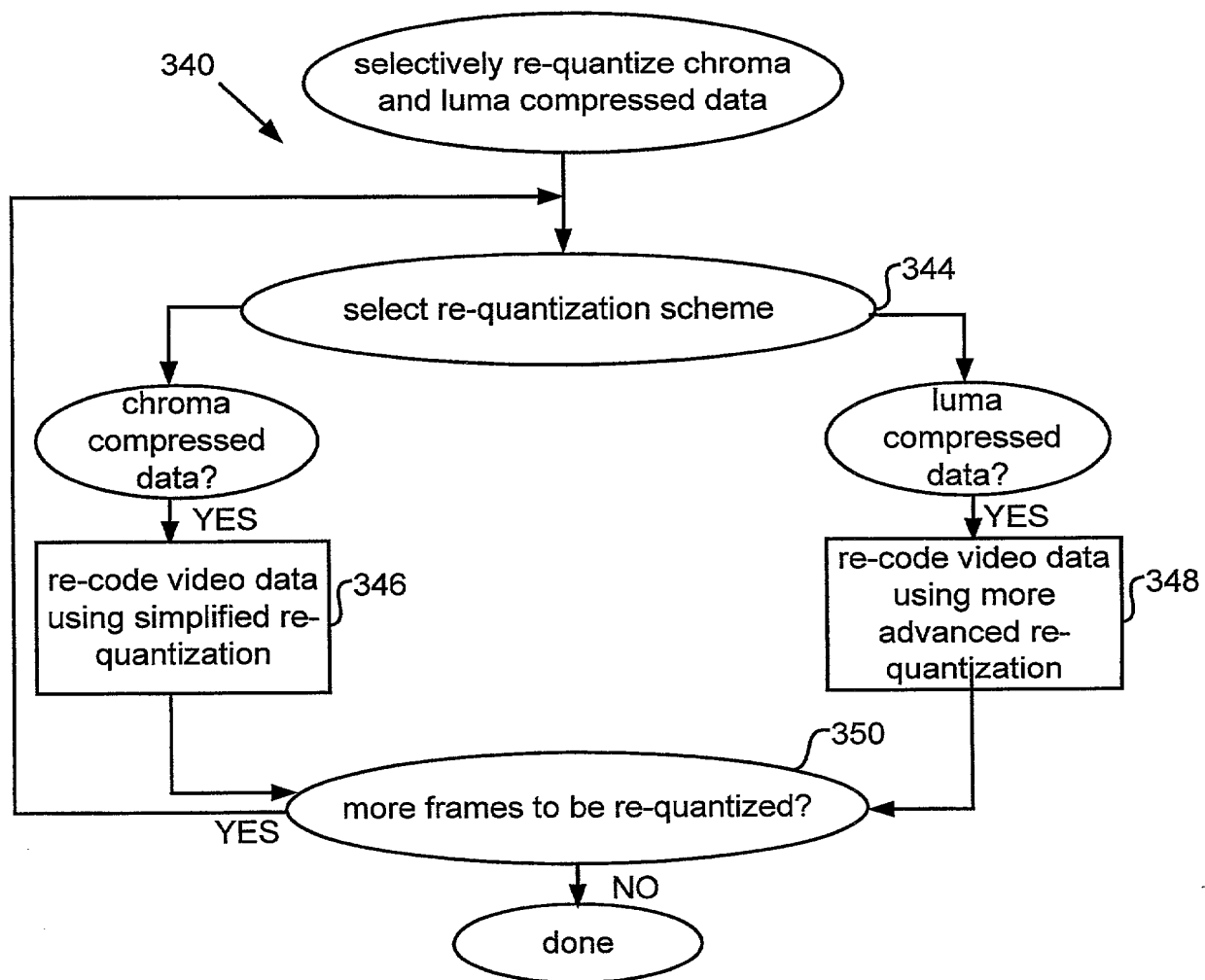


Figure 5C

360

364

362

Combination/ Ranking	More demanding Re-quantization (e.g., process flow 120)	Less demanding Re-quantization (e.g., process flow 100)
1	all frames	none
2	all I, P, and B luma frames	B chroma frames
3	all I and P frames	all B frames
4	most I and P luma and chroma frames	last P luma and chroma frame in GOP, all B frames
5	I frames, most P luma frames	last P luma frame in GOP, all B frames
6	none	all frames
7	none	all I chroma and luma frames
8	none	just I chroma frames
9	none	none

Figure 5D

FIG. 6 is a block diagram of a system 810 in accordance with one embodiment of the present invention. The system 810 includes a processor 863 and memory 862. The processor 863 is connected to the memory 862. The system 810 also includes interfaces 868. The interfaces 868 are connected to the processor 863 and the memory 862. The system 810 is connected to a network 815. The network 815 is connected to the processor 863 and the memory 862.

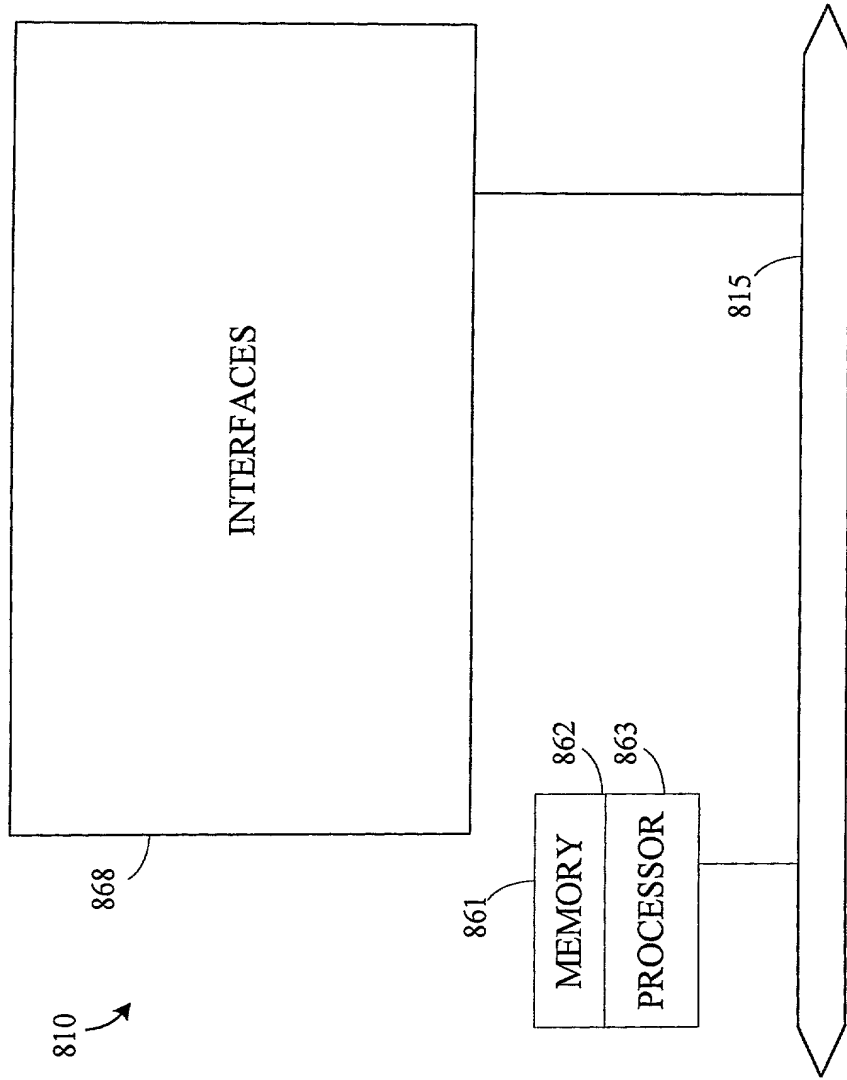


Figure 6